



## SM2258XT B17 NAND Flash F/W &amp; ISP Release Information – R0123A0

**Introduction**

This purpose of this document is to provide release information on the SM22X family F/W and ISP release information

**Fix Coverage**

- ☒ stands for the “**new fix**” or “**new support**” in the category  
☐ stands for the “**no-update**” in the category

<input checked="" type="checkbox"/> <b>MP Tool</b>	<input checked="" type="checkbox"/> <b>Controller ISP</b>
<input type="checkbox"/> <b>Yield Issue</b> <input type="checkbox"/> <b>Flash Issue</b> <input type="checkbox"/> TLC Flash <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others  <input checked="" type="checkbox"/> <b>3D NAND Flash</b> <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input checked="" type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others  <input type="checkbox"/> <b>MP Tool Bug Fix</b> <input checked="" type="checkbox"/> <b>MP Tool New Function</b> <input checked="" type="checkbox"/> <b>Feature Enhance</b>	<input checked="" type="checkbox"/> <b>Yield Issue</b> <input type="checkbox"/> <b>Flash Issue</b> <input type="checkbox"/> TLC Flash <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others  <input checked="" type="checkbox"/> <b>3D NAND Flash</b> <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input checked="" type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others  <input checked="" type="checkbox"/> <b>ISP Bug Fix</b> <input checked="" type="checkbox"/> <b>Feature Enhance</b>



## ISP Revision History

Package	ISP version	MP Tool Version	RDT version	Note
R0223A	R0123A0	R0124A	R0130A0	<p>ISP</p> <ol style="list-style-type: none"><li>1. Enlarge command queue depth for glntlvWay&gt;4</li><li>2. Erase wait status if glntlvWay==8</li><li>3. Power on ExtraGC will finish gc state when spare only have 2 block.</li><li>4. Distinguish read count threshold between SLC and non-SLC.</li><li>5. Fix read scrub issue which data stop count is not restored.</li><li>6. Fix bug of DDR200 return form power mode with wrong flash mode.</li></ol> <p>MPTool:</p> <ol style="list-style-type: none"><li>1. Display TranADJ timeout case, then show failed CE location.</li><li>2. Change "START" button UI to be the same as "Q0918A" : "Start\r\n(Space Key)".</li><li>3. Show "Idle time for background GC" option.</li><li>4. Show "Max Data Size for GC (GB)" option.</li><li>5. Microcode.flu added file size need to append "one plane page size" and IDTable Word234 and 235 also need to assign this value.</li><li>6. Add "Delay 30 minutes to read check" option at [RDT Test Setting] dialog.</li><li>7. Just display flash part name that package can support.</li><li>8. Create virtual FlashID.bin when click "Gen Linux Bin with All Parameter" or "Gen FFU File"</li></ol>
Q1124A	Q1115A0	Q1121A	Q1116A0	<p>ISP</p> <ol style="list-style-type: none"><li>1 First formal release to support Intel/Micron B17.</li></ol>

**Note:**

1. F/W and ISP update is recommended.
2. History # is denoted by "Version-Date" .

This document is for information only and is subject to change without notice. Silicon Motion, Inc. reserves the right to make changes in the product design without reservation and without notice to its users.